

Wiring, Printed - Component

COMPANY

SHENZHEN JETFGO CIRCUITS CO LTD
Tower A18, Xinxing Industrial Estate
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Bao'an
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E218213

Type	Cond Width		Max Report					Max							
	Min		Cond	SS/	Area	date	Surface	Assembly		Solder	Solder	Oper	Meets C		
	Min	Edge	Thk	DS/	Diam	After	Mount	Process	Process	Limits	Temp	Flame	UL796	T	
	mm	mm	mic	DSO	mm	2022-01-01	Technology	Temp °C	Cycles	°C	sec	°C	Class	DSR	I
Multilayer printed wiring boards															
JFG-04(ASP1)	0.075	0.075	12 Int:102	DS	76.2	Yes	Yes	260	3	288	20	130	V-0	All	*
JFG-ML	0.10	0.10	17 Int:68	DS	76.2	No	-	-	-	288	30	130	V-0	All	-
JS200501A	0.18	0.18	17 Int:34	DS	15	No	-	-	-	288	10	130	V-0	All	-
Single layer printed wiring boards															
JFG-02(ASP1)	0.075	0.075	12	DS	76.2	Yes	Yes	260	3	288	20	130	V-0	All	*
JFG-DS	0.10	0.10	17	DS	76.2	No	-	-	-	288	30	130	V-0	All	-
JFG01	0.06	0.18	17	DS	15	No	-	-	-	260	30	105	V-0	All	-

* - CTI marking is optional and may be marked on the printed wiring board.

- V-0 for uncoated JS200501A; V-1 for coated JS200501A

ASP1 - Assembly Solder process evaluated to IPC-TM-650, 2.6.27 Thermal Stress Assembly Simulation.

Marking: Company name or tradename "E218213" or trademark  or file number and type designation. May be followed by a suffix to denote factory identification or flammability classification..

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